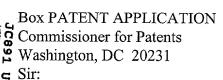
Docket No.: 50090-265

## UTILITY PATENT APPLICATION UNDER 37 CFR 1.53(b)





Transmitted herewith for filing is the patent application of:

INVENTOR: Kiyotoshi UEDA, Shoichi OOSHITA

FOR: METHOD AND APPARATUS FOR TESTING SEMICONDUCTOR INTEGRATED CIRCUIT, AND SEMICONDUCTOR INTEGRATED CIRCUIT MANUFACTURED THEREBY

Enclos	sed are:				
$\boxtimes$	20 pages of specification, claims, abstract.				
$\boxtimes$	Declaration and Power of Attorney.				
$\boxtimes$	Priority Claimed.				
	Certified copy of Japanese Patent Application 2000-216321				
$\boxtimes$	7 sheets of formal drawing.				
$\bowtie$	An assignment of the invention to Mitsubishi Denki Kabushiki Kaisha				
	and the assignment recordation fee.				
	An associate power of attorney.				
	A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.				
$\boxtimes$	Information Disclosure Statement, Form PTO-1449 and reference.				
	Return Receipt Postcard				

The filing fee has been calculated as shown below:

	NO. OF		EXTRA		17.000
Total Control	CLAIMS		CLAIMS	RATE	AMOUNT
Total Claims	14	-20	0	\$18.00	\$0.00
Independent Claims	2	-3	0	\$80.00	\$0.00
	\$0.00				
	\$710.00				
	\$710.00				
	\$0.00				
	\$40.00				
THE THE	Total Fee	\$750.00			



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PATENT TRADEMARK OFFICE



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Any additional filing fees required under 37 CFR 1.16.

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Any patent application processing fees under 37 CFR 1.17.

Any filing fees under 37 CFR 1.16 for presentation of extra claims.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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**Date: January 23, 2001** Facsimile: (202) 756-8087